

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. MICRON.165DV1	APPLICATION NO. 10/659,728
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)		APPLICANT HU, Yongjun Jeff	
		FILING DATE September 10, 2003	GROUP Unknown

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	1	5,854,115	12/29/98	Gardner et al.	438	305	
	2	5,960,319	09/28/99	Iwata et al.	438	664	
	3	6,013,546	01/11/00	Gardner et al.	438	231	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
pa	4. "Silicon Processing for the VLSI Era" by Stanley Wolf Ph.D.; Volume 2: Process Integration; Lattice Press (1990); ISBN 0-961672-4-5; Pages 301-304

R:\DOCS\JWAUWA-7067.DOC : ac
120903

EXAMINER	<i>[Signature]</i>	DATE CONSIDERED	7/27/04
*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.			